



Cycle-based Programming of Distributed Systems: The Synchrony Hypothesis

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Overview

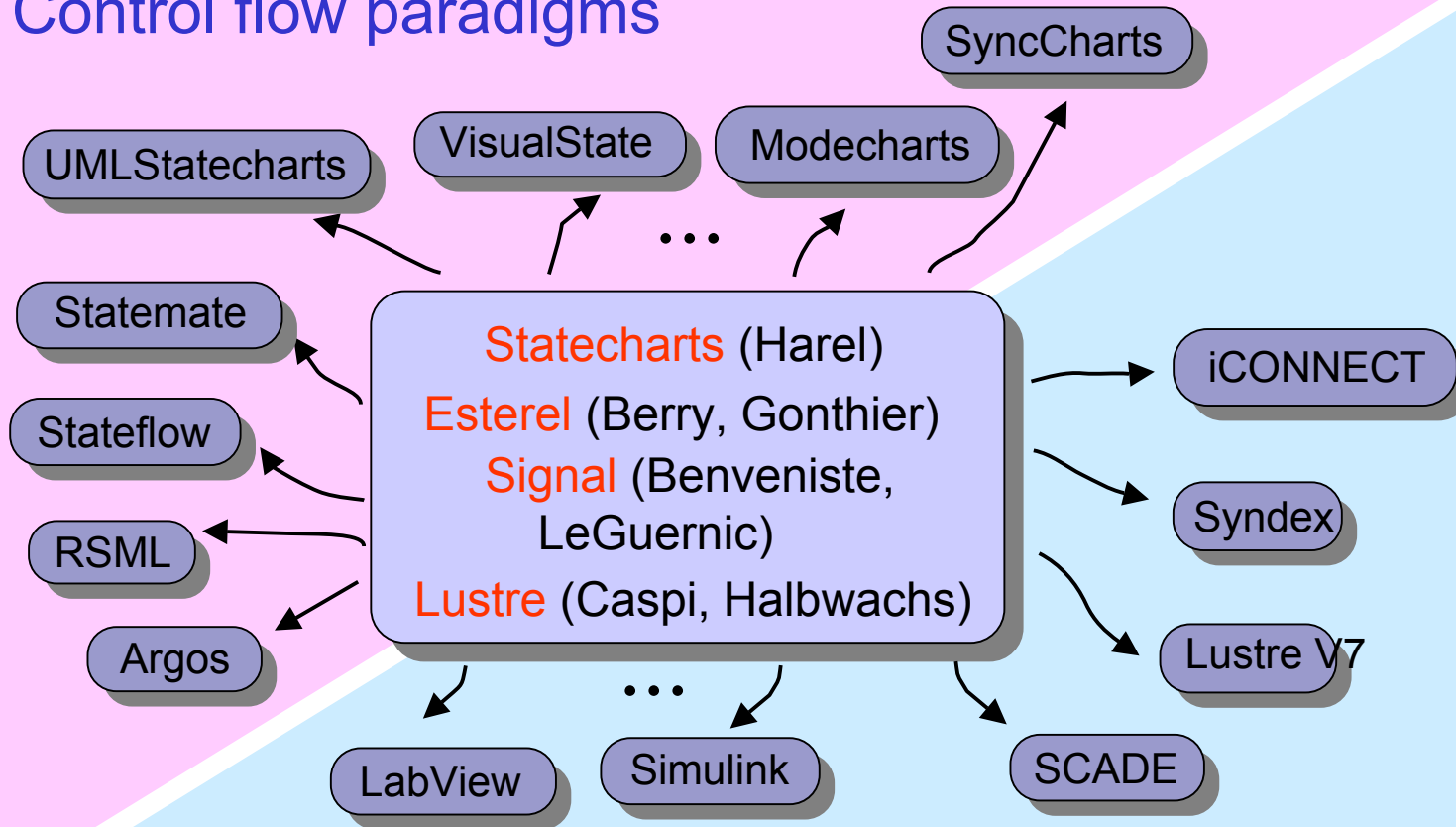
1. Synchronous Programming
2. The Synchrony Hypothesis
3. Causal Reaction = Fixed Point ?
4. What's in a Step ? : Notions of Causality
5. The Synchrony Hypothesis (Hypo-)Thesis



1. Synchronous Programming

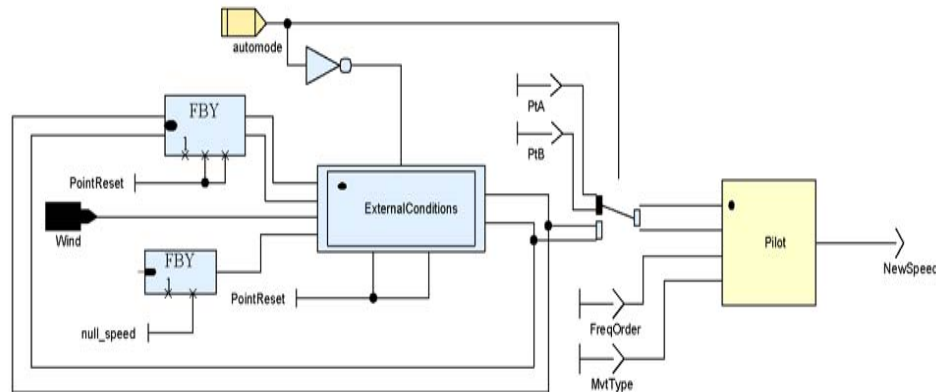
Synchronous Programming

Control flow paradigms

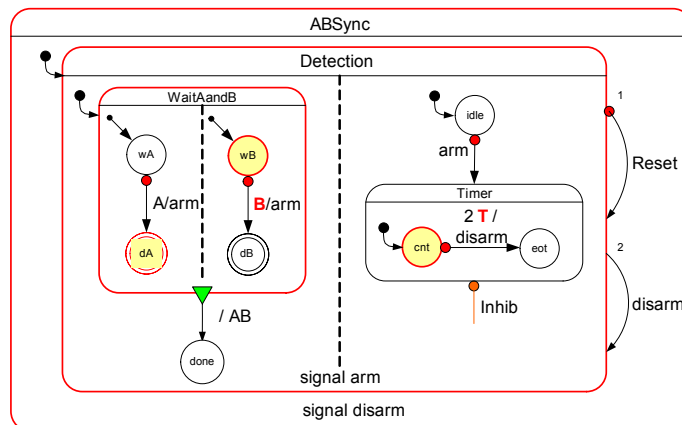


Data flow paradigms

Example: SCADE – Esterel Tech



Data Flow: SCADE Lustre

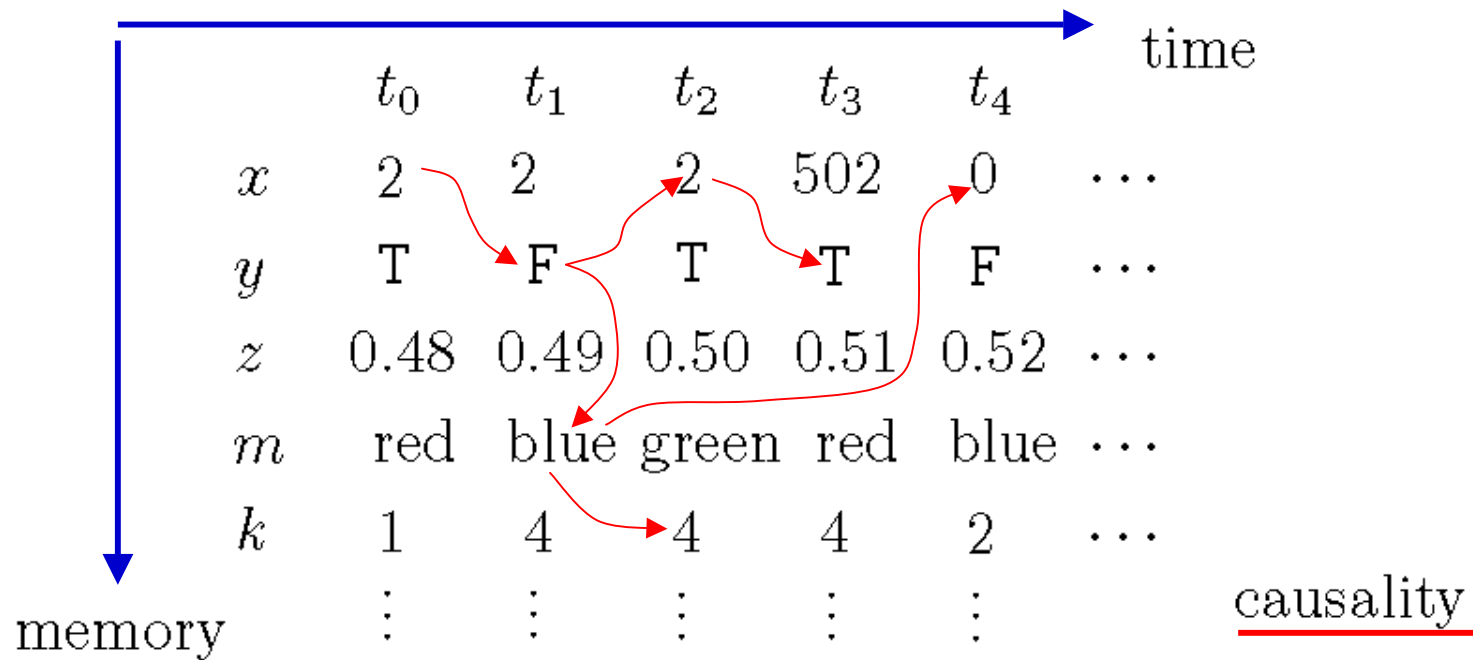


Control Flow: SCADE Safe State Machines

- embedded systems domain (avionics, automotive)
- rigorous semantics
- verification & testing (certification)
- code-generation
- hw/sw codesign

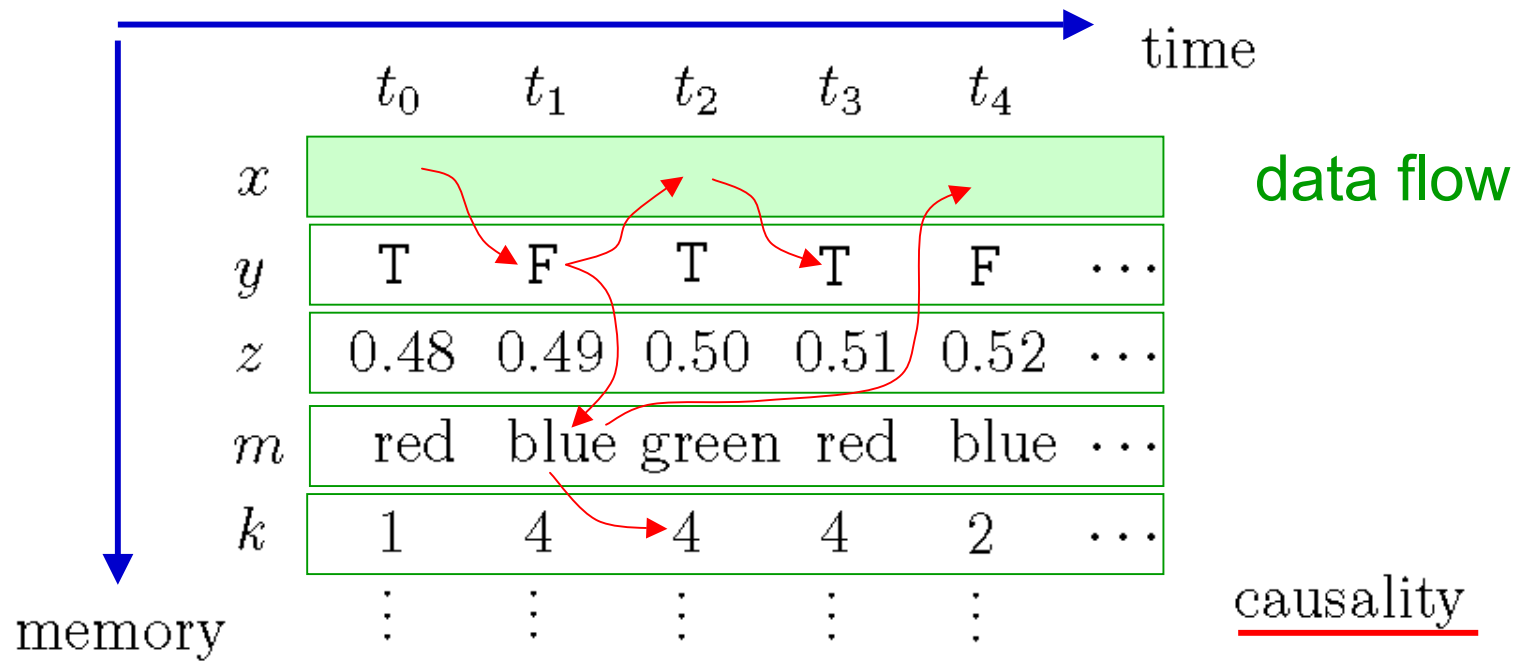


Orthogonality in Time and Space



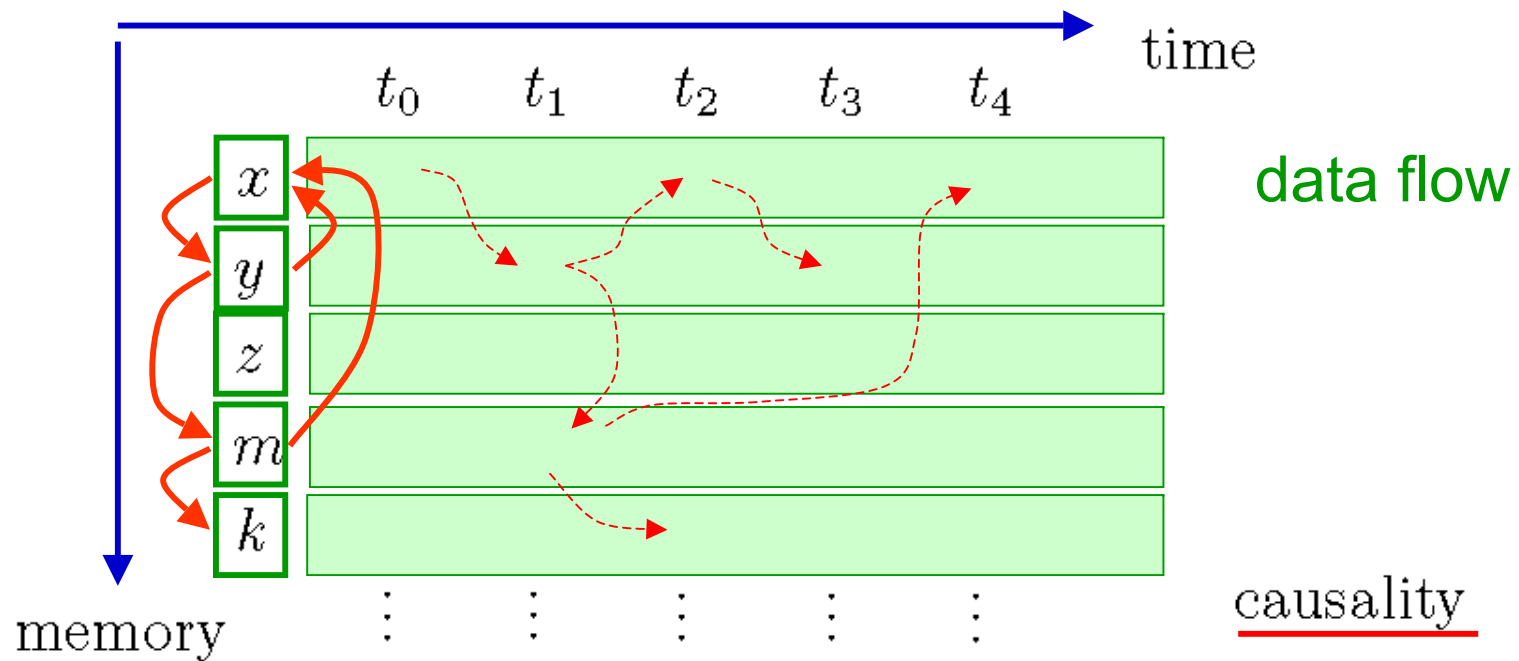


Data Flow



Data Flow

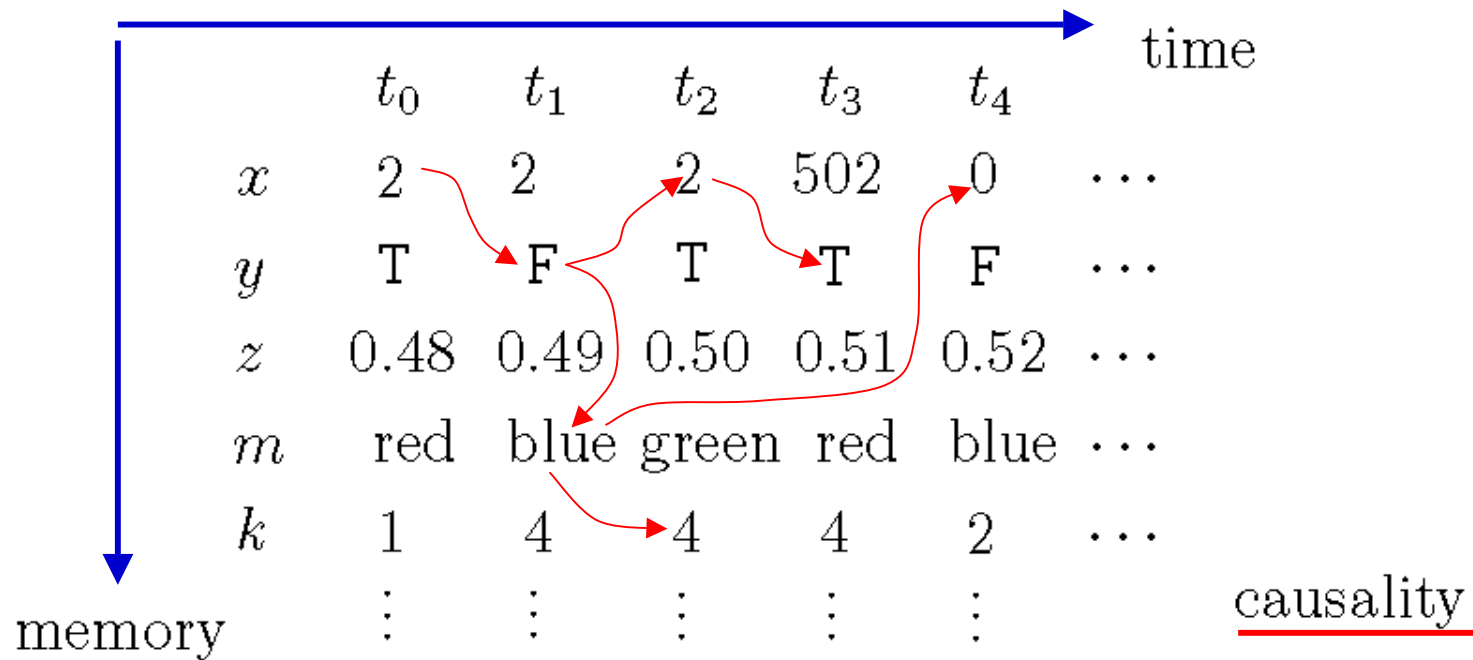
Q: How do we treat the cyclic DF dependencies ?



A: **Continuity Hypothesis**, Kahn stream semantics !

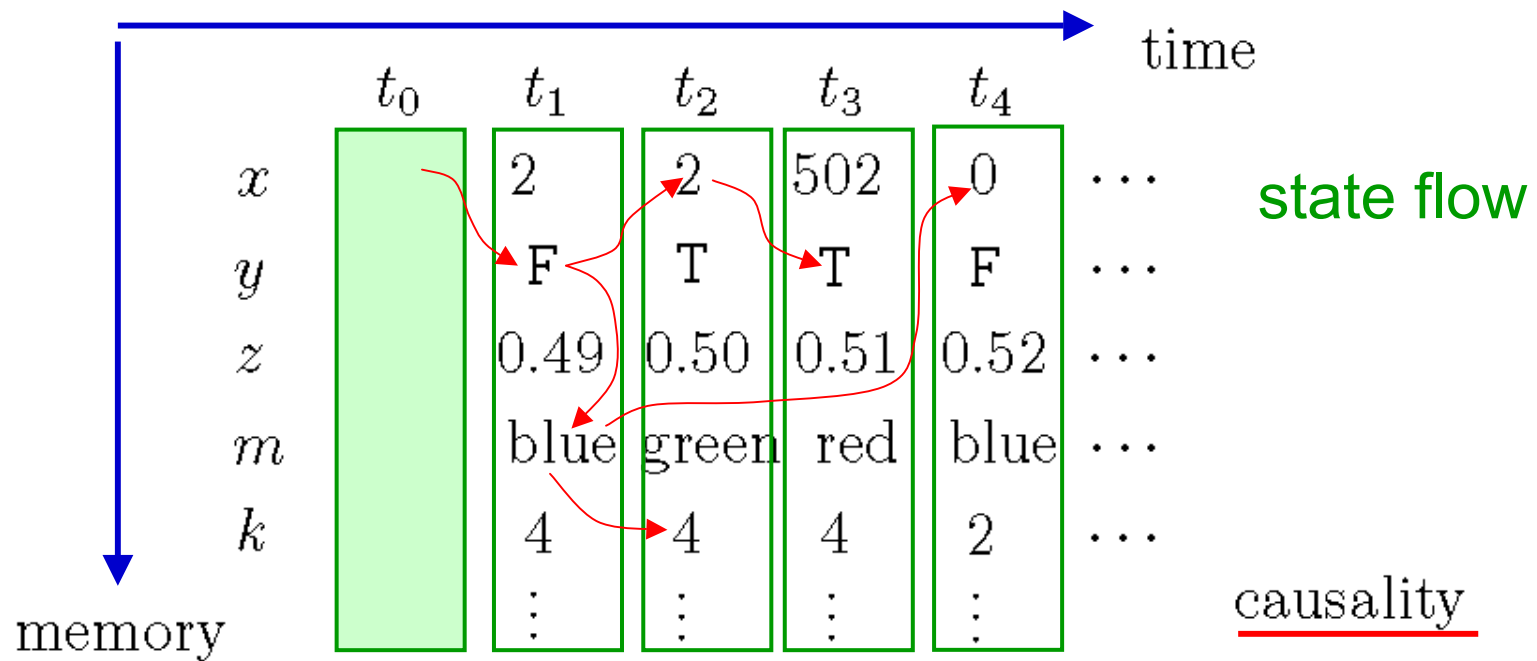


Orthogonality in Time and Space



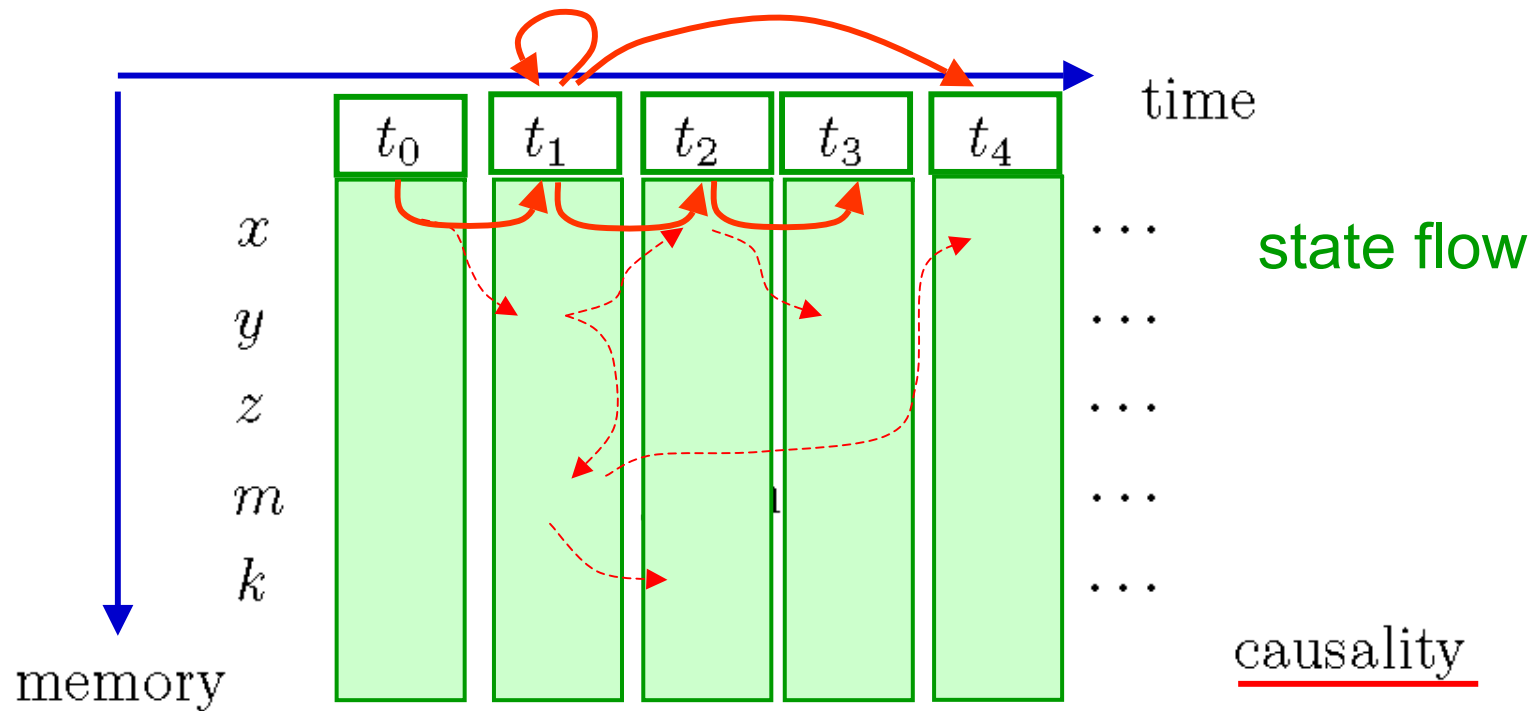


State Flow



State Flow

Q: How do we treat the cyclic SF dependencies ?



A: **Synchrony Hypothesis**, Fourman response semantics



2. The Synchrony Hypothesis

Synchrony Hypothesis

Environment view:

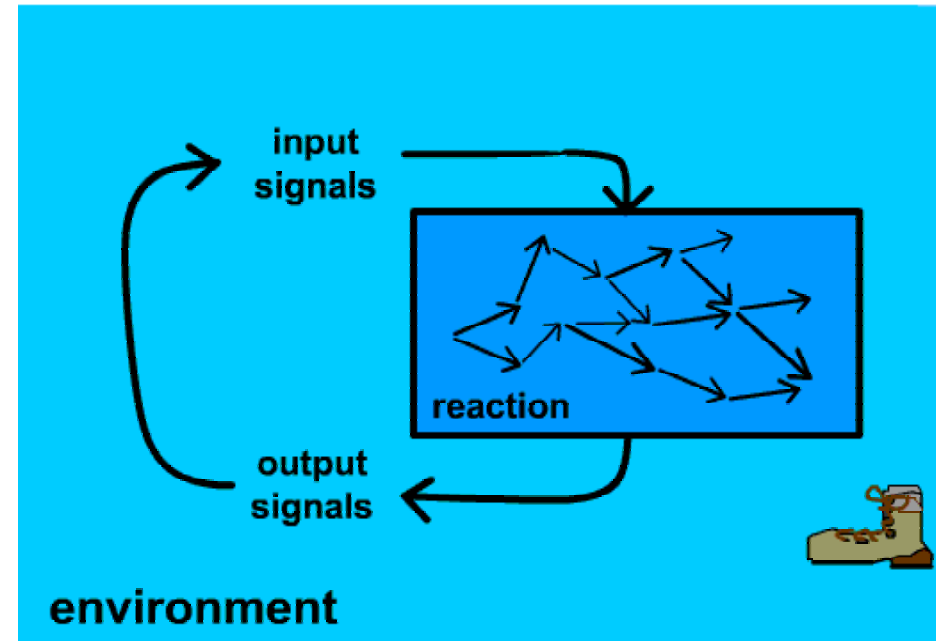
Reactions are

- atomic
- deterministic
- bounded

System view:

Reactions may be

- non-atomic
- non-deterministic
- unbounded



*“A reactive system is **faster than** its environment, hence reactions can be considered atomic”*

The Synchrony Paradox

Environment view:

Reactions are

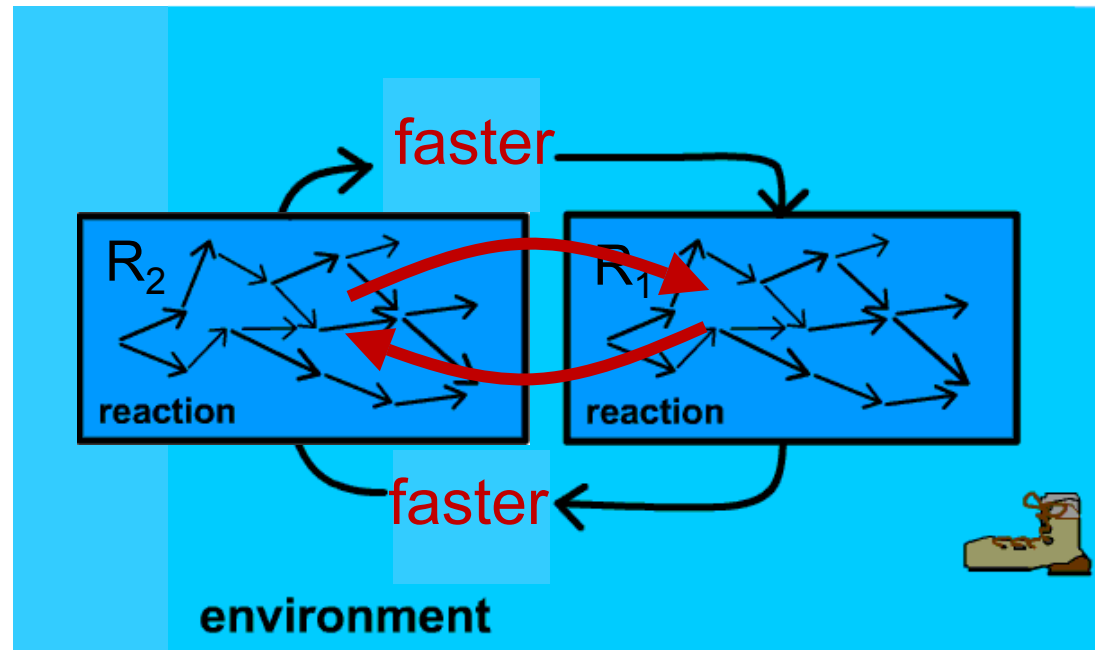
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↕ **Paradox ?**

System view:

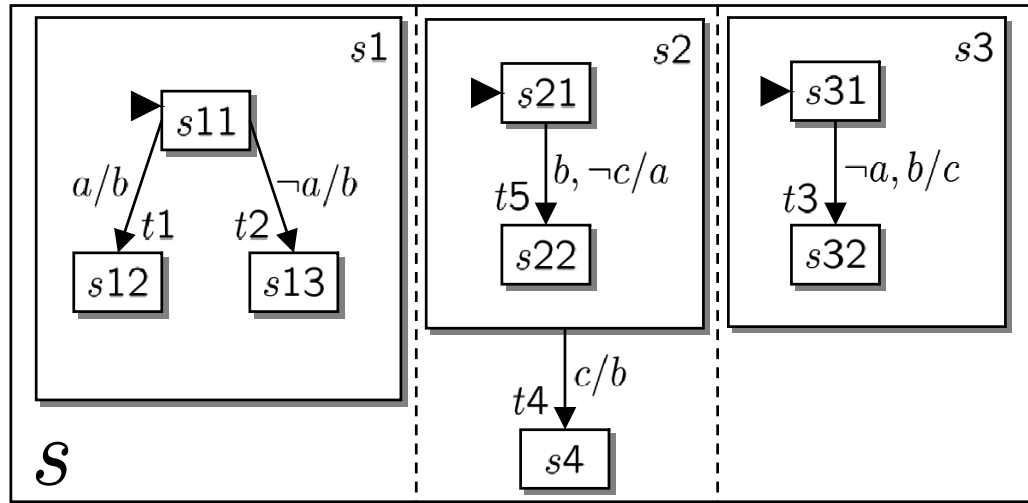
Reactions may be

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*“A reactive system is **faster than** its environment, hence reactions can be considered atomic”*

Programming Synchronous Reactions



- logical transitions
- conjunctions = parallelism
- negations code choices, priorities and hierarchy

REACT :=

$$t1 \supset b \wedge t2 \supset b \wedge t3 \supset c \wedge t4 \supset b \wedge t5 \supset a \wedge$$

$$(s11 \wedge a \wedge \neg t2) \supset t1 \wedge$$

$$(s11 \wedge \neg a \wedge \neg t1) \supset t2 \wedge$$

$$(s31 \wedge \neg a \wedge b) \supset t3 \wedge$$

$$(s2 \wedge c) \supset t4 \wedge$$

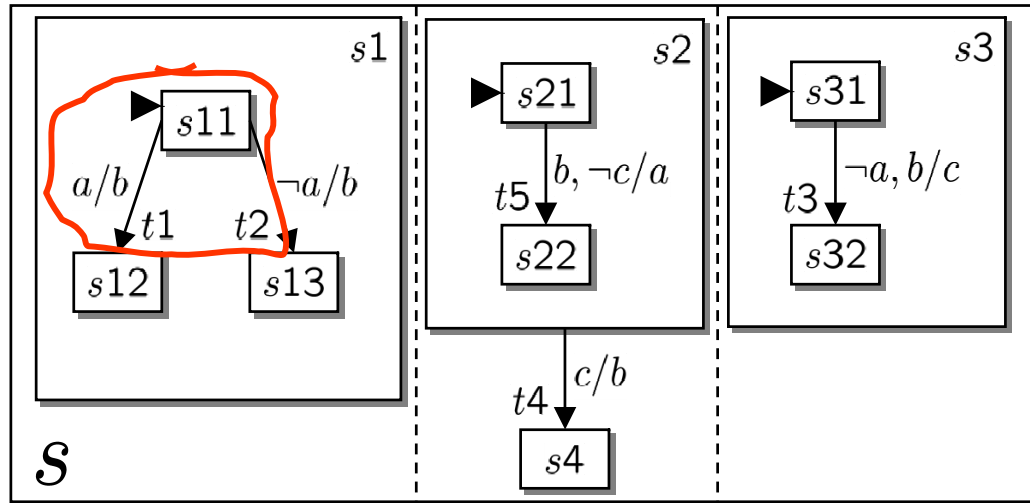
$$(s21 \wedge b \wedge \neg c \wedge \neg t4) \supset t5$$

parallelism

logical transitions

choice, priority

Programming Synchronous Reactions

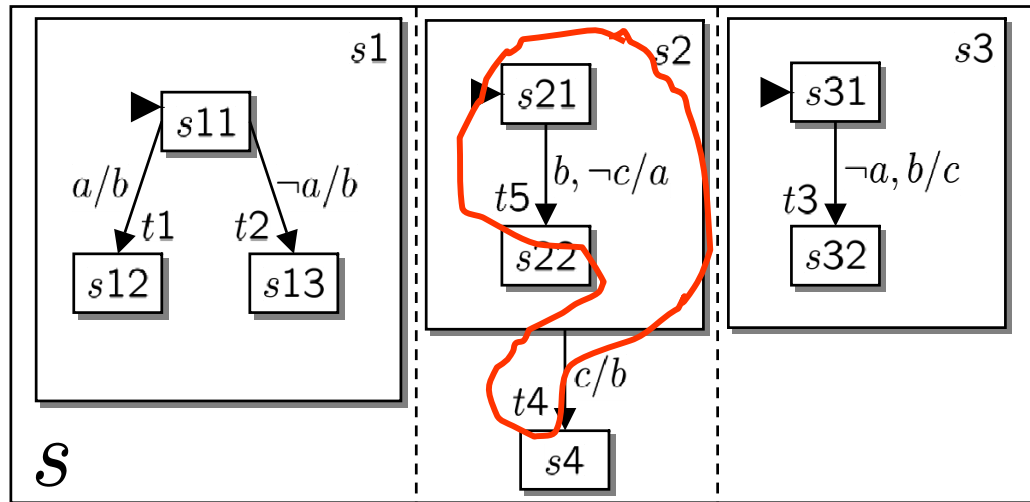


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Programming Synchronous Reactions



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REACT :=

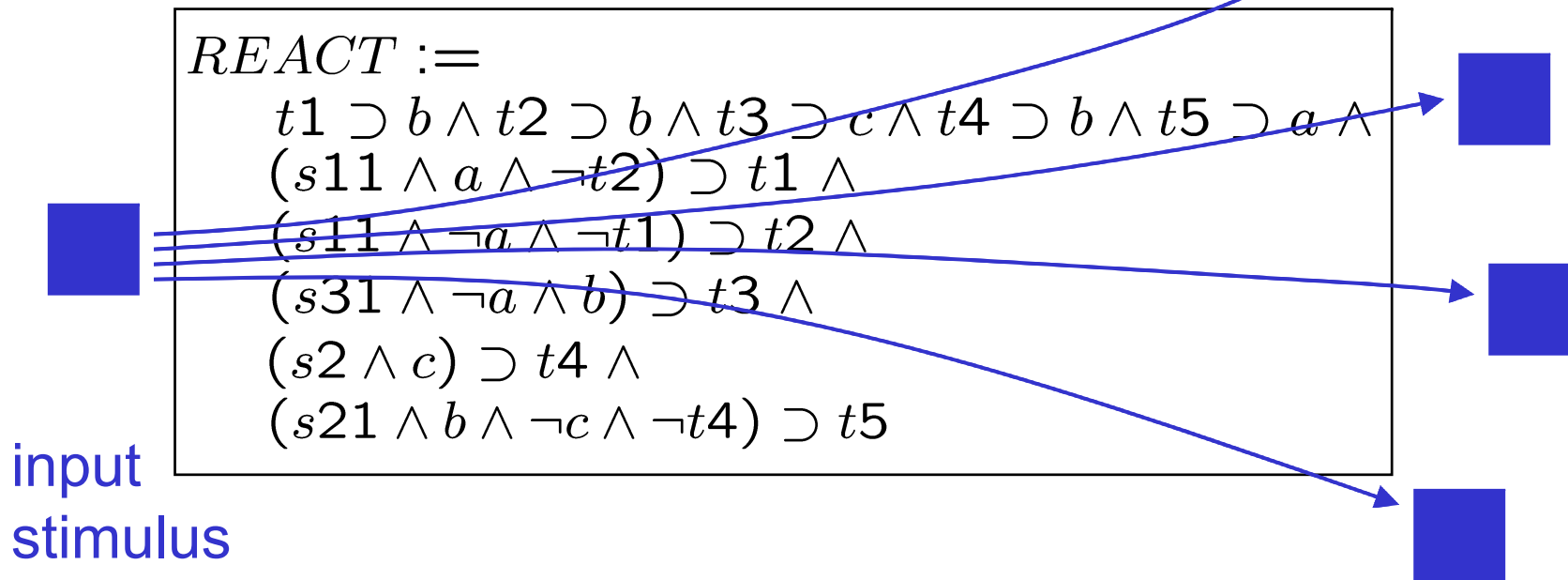
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 \end{aligned}$$



Synchronous Abstraction

In which sense does **REACT** describe an **atomic macro step** ?

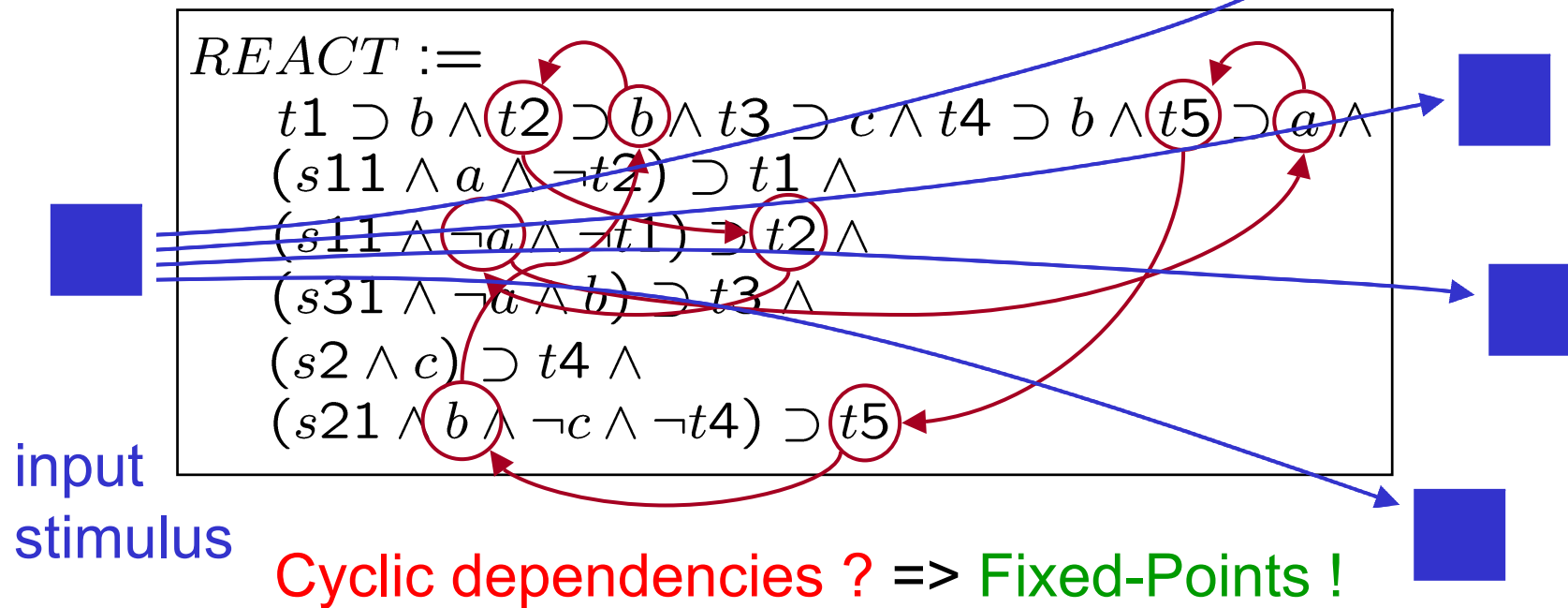
instantaneous reaction



Synchronous Abstraction

In which sense does **REACT** describe an **atomic macro step** ?

instantaneous reaction





3. Causal Reaction = Fixed-Point ?



Synchronous Reactive Component

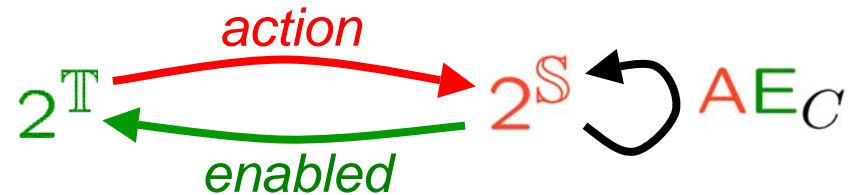
Reactive component

$$C = (\mathbb{S}, \mathbb{T}, pos, neg, act)$$

\mathbb{S}, \mathbb{T} atomic logical signals, logical transitions

$pos, neg, act : \mathbb{T} \rightarrow 2^{\mathbb{S}}$ positive, negative triggers, actions

Response of C



$$action(T) = \{s \mid \exists t \in T. s \in act(t)\} \quad T \subseteq \mathbb{T}$$

$$enabled(S) = \{t \mid pos(t) \subseteq S \wedge neg(t) \subseteq \bar{S}\} \quad S \subseteq \mathbb{S}$$

$$AE_C(S) = action(enabled(S)) \quad \text{„response function“}$$



Reaction = Fixed-Point ?

Logical Coherence [Berry]

"A signal s is present in an instant if and only if an ``emit s'` statement is executed in this instant."

Logical Coherence & Reactiveness

- A response S is **logically coherent** iff S is a **fixed-point** of AE_C , i.e., $S = AE_C(S)$.
- C is **logically reactive** iff in every activation state and environment, AE_C has a **fixed-point**.



Causal Response = Unique Fixed Point ?

Problem

The response function

$$AE_C(S) = \text{action}(\{t \mid \text{pos}(t) \subseteq S \wedge \text{neg}(t) \subseteq \bar{S}\})$$

is **not monotonic** !



Causal Response = Unique Fixed Point ?

Problem

The response function

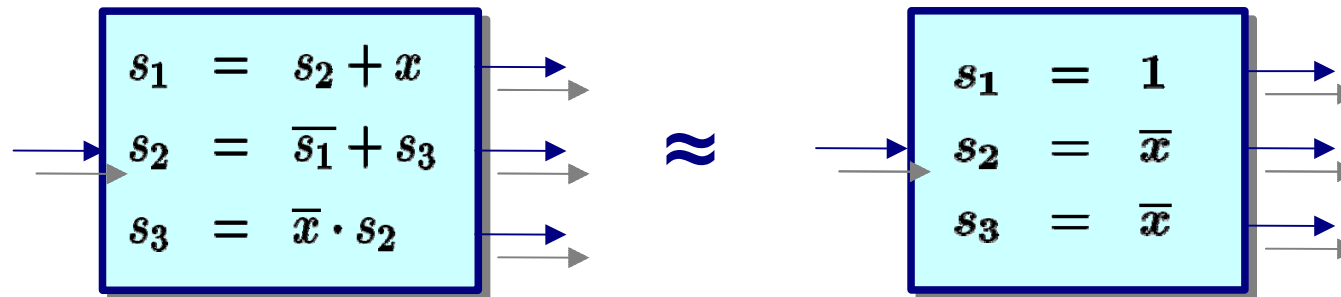
$$AE_C(S) = action(\{t \mid pos(t) \subseteq S \wedge neg(t) \subseteq \overline{S}\})$$

covariant *contravariant*
↓ ↓

is **not monotonic** !

- no unique (least) fixed points !
- compositionality and full-abstraction problems !
- different computation methods !
→ different notions of steps, instants, reactions ...

Example

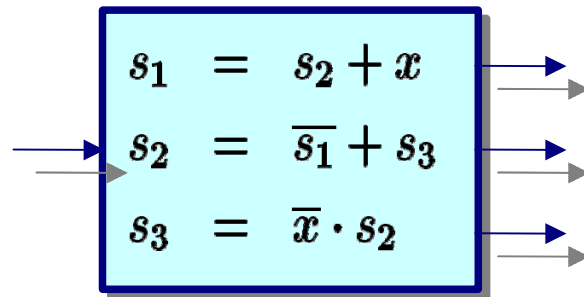


For all inputs there is a **unique stationary Boolean fixed point**. Thus, the system is **logically reactive**.

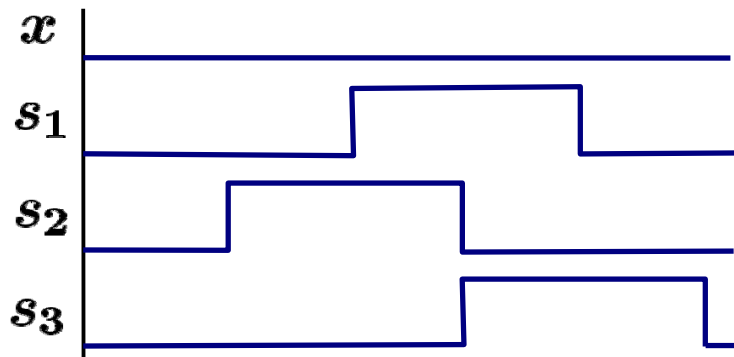
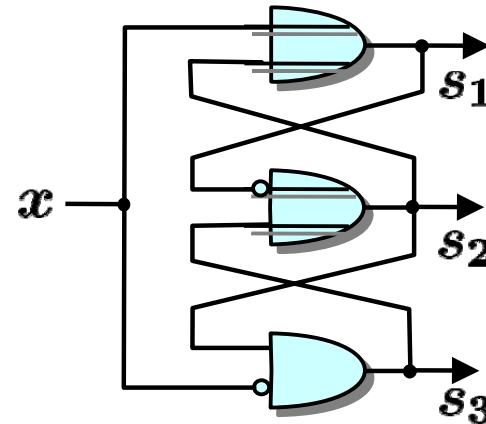
We can **compile & execute** Boolean solution **atomically** !

But what if we are compiling for a **component-based** and **distributed architecture** ?

Example

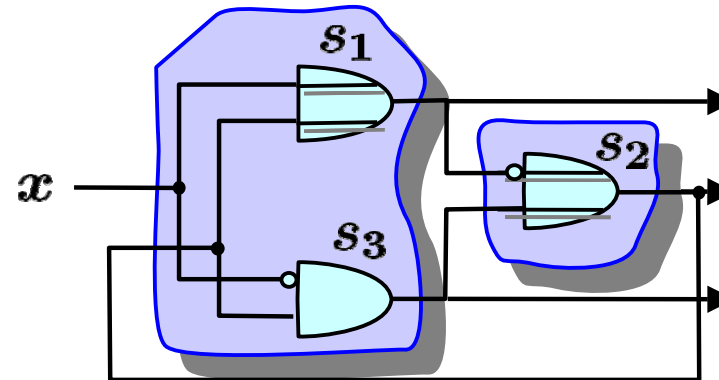
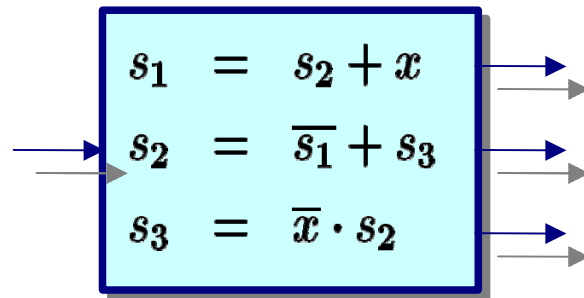


≠



Oscillation under
up-bounded
inertial delay
scheduling
[Brzozowski & Seger]

Example



Oscillation can be avoided if we

- schedule s_1, s_3 with higher priority than s_2 or
- implement s_1, s_3 atomically, as a 2in/2out block.

Then, whenever s_2 is executed, we maintain the invariant

$$s_2 = \overline{s_1} + s_3 = \overline{x}$$



4. What is in a Step ? Notions of Causality



What is in a Step ? - A Profusion of Options

1 Avoid Negations

- **only positive triggers** [Modecharts '94, Argos '89]

2 Modify Semantics of Negation

- **give up global consistency** [Huizing&al. '88, Modecharts '96]
- **add consistency as implicit trigger**
[Maggiolo-Schettini &al. '96, Lüttgen &al. '99]

3 Give up Synchrony Hypothesis (no abstraction)

- **all signals delayed**[Statemate '90,VHDL,RSML '95,PretC '09]
- **negative triggers delayed** [Saraswat TCCP '94,
Boussinot & deSimone SL '95, Boussinot FunLoft'07]



What is in a Step ? - A Profusion of Options

4 Conflict-avoiding Schedules

- only accept **stratifiable** (statically schedulable) programs [Normal Logic Programming]
- **sequential schedule (endochrony)** [Benveniste & al.'00]
- **NRSA** „no reaction to signal absence“ (**weak endochrony, concurrent input reading**) [Butucaru, Caillaud'06]



What is in a Step ? - A Profusion of Options

5 Self-scheduled Run-time (explicit absence, dual rail)

- **non-deterministic speculation** on absence
[Pnueli & Shalev '91; Boussinot's „basic semantics“ '98]



What is in a Step ? - A Profusion of Options

5 Self-scheduled Run-time (explicit absence, dual rail)

- **non-deterministic speculation** on absence
[Pnueli & Shalev '91; Boussinot's „basic semantics“ '98]
„Feel free to assume the absence of a signal as long as it is consistent to do so; if necessary, backtrack!“
- fully-abstract, compositional **intuitionistic Kripke semantics**
[Lüttgen & Mendler '01]
- game-theoretic **“lazy“ fixed-points** [Aguado & Mendler '05]



What is in a Step ? - A Profusion of Options

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- constructiveness = “computed“ absence [Berry '00]



What is in a Step ? - A Profusion of Options

5 Self-scheduled Run-time (explicit absence, dual rail)

- **non-deterministic speculation** on absence
[Pnueli & Shalev '91; Boussinot's „basic semantics“ '98]
- **constructiveness** = “computed” absence [Berry '00]
“Accept the absence of a signal only under computable evidence that it may not occur later“
 - game-theoretic “eager” fixed-points [Aguado & Mendler '05]
 - **delay-insensitivity** = non-inertial delay
= **constructive modal logic** [Mendler & Shiple & Berry '07]
- **SugarCubes** [Boussinot '98] (Esterel v3,v4,v5,v6,v7)
- & many other **hardware approaches**
 - speed-independence, semi-modularity, distributivity, ...



5. The Synchrony Hypothesis Thesis



Outlook

Thesis 1

There are as many notions of constructive causality as there are scheduling/run-time models

Thesis 2

Synchronous reaction requires intensional semantics:

classical Boolean logic

⇒ **constructive logic** (e.g., Heyting algebra)

least and greatest fixed points

⇒ general **game-theoretic fixed points**



Thank You !